



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gregory E. Howard, et al.

Art Unit: 2811

Serial No.: 10/614,851

Examiner: TBD

Filed: 07/08/03

Docket: TI-35904

For: Semiconductor Packages for Enhanced Number of Terminals, Speed and Power Performance

LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz
Karen Vertz

10-14-03
Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are **THREE (3)** sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265
Phone: (972) 917-5653

Respectfully submitted,

Michael K. Skrehot
Reg. No. 36,682
Attorney for Applicants